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## KEY=EXERCISES - BRENDAN MARISSA

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### Computer Organization and Design The Hardware/software Interface, ARM Edition

### Computer Organization and Design The Hardware/Software Interface

*Elsevier "Presents the fundamentals of hardware technologies, assembly language, computer arithmetic, pipelining, memory hierarchies and I/O"--*

### Computer Organization and Design RISC-V Edition

### The Hardware Software Interface

*Morgan Kaufmann The new RISC-V Edition of Computer Organization and Design features the RISC-V open source instruction set architecture, the first open source architecture designed to be used in modern computing environments such as cloud computing, mobile devices, and other embedded systems. With the post-PC era now upon us, Computer Organization and Design moves forward to explore this generational change with examples, exercises, and material highlighting the*

*emergence of mobile computing and the Cloud. Updated content featuring tablet computers, Cloud infrastructure, and the x86 (cloud computing) and ARM (mobile computing devices) architectures is included. An online companion Web site provides advanced content for further study, appendices, glossary, references, and recommended reading. Features RISC-V, the first such architecture designed to be used in modern computing environments, such as cloud computing, mobile devices, and other embedded systems Includes relevant examples, exercises, and material highlighting the emergence of mobile computing and the cloud*

## Computer Architecture

### A Quantitative Approach

*Elsevier The computing world today is in the middle of a revolution: mobile clients and cloud computing have emerged as the dominant paradigms driving programming and hardware innovation today. The Fifth Edition of Computer Architecture focuses on this dramatic shift, exploring the ways in which software and technology in the cloud are accessed by cell phones, tablets, laptops, and other mobile computing devices. Each chapter includes two real-world examples, one mobile and one datacenter, to illustrate this revolutionary change. Updated to cover the mobile computing revolution Emphasizes the two most important topics in architecture today: memory hierarchy and parallelism in all its forms. Develops common themes throughout each chapter: power, performance, cost, dependability, protection, programming models, and emerging trends ("What's Next") Includes three review appendices in the printed text. Additional reference appendices are available online. Includes updated Case Studies and completely new exercises.*

## Computer Organization and Design

### The Hardware/Software Interface, Third Edition

*Elsevier This best selling text on computer organization has been thoroughly updated to reflect the newest technologies. Examples highlight the latest processor designs, benchmarking standards, languages and tools. As with previous editions, a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system. The book presents an entire MIPS instruction set—instruction by instruction—the fundamentals of assembly language, computer arithmetic, pipelining, memory hierarchies and I/O. A new aspect of the third edition is the explicit connection between program performance and CPU performance. The authors show how hardware and software components--such as the specific algorithm, programming language, compiler, ISA and processor implementation--impact program performance. Throughout the book a new feature*

*focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system. The book digs deeper into the hardware/software interface, presenting a complete view of the function of the programming language and compiler--crucial for understanding computer organization. A CD provides a toolkit of simulators and compilers along with tutorials for using them. For instructor resources click on the grey "companion site" button found on the right side of this page. This new edition represents a major revision. New to this edition: \* Entire Text has been updated to reflect new technology \* 70% new exercises. \* Includes a CD loaded with software, projects and exercises to support courses using a number of tools \* A new interior design presents defined terms in the margin for quick reference \* A new feature, "Understanding Program Performance" focuses on performance from the programmer's perspective \* Two sets of exercises and solutions, "For More Practice" and "In More Depth," are included on the CD \* "Check Yourself" questions help students check their understanding of major concepts \* "Computers In the Real World" feature illustrates the diversity of uses for information technology \*More detail below...*

# Computer Architecture

## A Quantitative Approach

*Morgan Kaufmann Computer Architecture: A Quantitative Approach, Sixth Edition has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every*

*chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry*

## Computer Networks

## A Systems Approach

## MIPS Assembly Language Programming

*Pearson Users of this book will gain an understanding of the fundamental concepts of contemporary computer architecture, starting with a Reduced Instruction Set Computer (RISC). An understanding of computer architecture needs to begin with the basics of modern computer organization. The MIPS architecture embodies the fundamental design principles of all contemporary RISC architectures. This book provides an understanding of how the functional components of modern computers are put together and how a computer works at the machine-language level. Well-written and clearly organized, this book covers the basics of MIPS architecture, including algorithm development, number systems, function calls, reentrant functions, memory-mapped I/O, exceptions and interrupts, and floating-point instructions. For employees in the field of systems, systems development, systems analysis, and systems maintenance.*

## ARM System Developer's Guide

## Designing and Optimizing System Software

*Elsevier Over the last ten years, the ARM architecture has become one of the most pervasive architectures in the world, with more than 2 billion ARM-based processors embedded in products ranging from cell phones to automotive braking systems. A world-wide community of ARM developers in semiconductor and product design companies includes software developers, system designers and hardware engineers. To date no book has directly addressed their need to develop the system and software for an ARM-based system. This text fills that gap. This book provides a comprehensive description of the operation of the ARM core from a developer's*

*perspective with a clear emphasis on software. It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code. Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software. The book covers both the ARM and Thumb instruction sets, covers Intel's XScale Processors, outlines distinctions among the versions of the ARM architecture, demonstrates how to implement DSP algorithms, explains exception and interrupt handling, describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques. A final chapter looks forward to the future of the ARM architecture considering ARMv6, the latest change to the instruction set, which has been designed to improve the DSP and media processing capabilities of the architecture. \* No other book describes the ARM core from a system and software perspective. \* Author team combines extensive ARM software engineering experience with an in-depth knowledge of ARM developer needs. \* Practical, executable code is fully explained in the book and available on the publisher's Website. \* Includes a simple embedded operating system.*

## Introduction to Embedded Systems, Second Edition

### A Cyber-Physical Systems Approach

*MIT Press An introduction to the engineering principles of embedded systems, with a focus on modeling, design, and analysis of cyber-physical systems. The most visible use of computers and software is processing information for human consumption. The vast majority of computers in use, however, are much less visible. They run the engine, brakes, seatbelts, airbag, and audio system in your car. They digitally encode your voice and construct a radio signal to send it from your cell phone to a base station. They command robots on a factory floor, power generation in a power plant, processes in a chemical plant, and traffic lights in a city. These less visible computers are called embedded systems, and the software they run is called embedded software. The principal challenges in designing and analyzing embedded systems stem from their interaction with physical processes. This book takes a cyber-physical approach to embedded systems, introducing the engineering concepts underlying embedded systems as a technology and as a subject of study. The focus is on modeling, design, and analysis of cyber-physical systems, which integrate computation, networking, and physical processes. The second edition offers two new chapters, several new exercises, and other improvements. The book can be used as a textbook at the advanced undergraduate or introductory graduate level and as a professional reference for practicing engineers and computer scientists. Readers should have some familiarity with machine structures, computer programming, basic discrete mathematics and algorithms, and signals and systems.*

# Interconnection Networks

*Morgan Kaufmann Foreword -- Foreword to the First Printing -- Preface -- Chapter 1 -- Introduction -- Chapter 2 -- Message Switching Layer -- Chapter 3 -- Deadlock, Livelock, and Starvation -- Chapter 4 -- Routing Algorithms -- Chapter 5 -- CollectiveCommunicationSupport -- Chapter 6 -- Fault-Tolerant Routing -- Chapter 7 -- Network Architectures -- Chapter 8 -- Messaging Layer Software -- Chapter 9 -- Performance Evaluation -- Appendix A -- Formal Definitions for Deadlock Avoidance -- Appendix B -- Acronyms -- References -- Index.*

# Modern Computer Arithmetic

*Cambridge University Press Modern Computer Arithmetic focuses on arbitrary-precision algorithms for efficiently performing arithmetic operations such as addition, multiplication and division, and their connections to topics such as modular arithmetic, greatest common divisors, the Fast Fourier Transform (FFT), and the computation of elementary and special functions. Brent and Zimmermann present algorithms that are ready to implement in your favourite language, while keeping a high-level description and avoiding too low-level or machine-dependent details. The book is intended for anyone interested in the design and implementation of efficient high-precision algorithms for computer arithmetic, and more generally efficient multiple-precision numerical algorithms. It may also be used in a graduate course in mathematics or computer science, for which exercises are included. These vary considerably in difficulty, from easy to small research projects, and expand on topics discussed in the text. Solutions to selected exercises are available from the authors.*

# Computer Organization

*New York ; Toronto : McGraw-Hill*

# Computer Systems

# Digital Design, Fundamentals of Computer Architecture and Assembly Language

*Springer This textbook covers digital design, fundamentals of computer architecture, and assembly language. The book starts by introducing basic number systems, character coding, basic knowledge in digital design, and components of a computer. The book goes on to discuss information representation in computing; Boolean algebra and logic gates; sequential logic; input/output; and CPU performance. The author also covers ARM architecture, ARM instructions and ARM assembly language*

which is used in a variety of devices such as cell phones, digital TV, automobiles, routers, and switches. The book contains a set of laboratory experiments related to digital design using Logisim software; in addition, each chapter features objectives, summaries, key terms, review questions and problems. The book is targeted to students majoring Computer Science, Information System and IT and follows the ACM/IEEE 2013 guidelines. • Comprehensive textbook covering digital design, computer architecture, and ARM architecture and assembly • Covers basic number system and coding, basic knowledge in digital design, and components of a computer • Features laboratory exercises in addition to objectives, summaries, key terms, review questions, and problems in each chapter

## Parallel Computer Architecture

### A Hardware/Software Approach

*Gulf Professional Publishing* This book outlines a set of issues that are critical to all of parallel architecture--communication latency, communication bandwidth, and coordination of cooperative work (across modern designs). It describes the set of techniques available in hardware and in software to address each issues and explore how the various techniques interact.

## Principles and Practices of Interconnection Networks

*Elsevier* One of the greatest challenges faced by designers of digital systems is optimizing the communication and interconnection between system components. Interconnection networks offer an attractive and economical solution to this communication crisis and are fast becoming pervasive in digital systems. Current trends suggest that this communication bottleneck will be even more problematic when designing future generations of machines. Consequently, the anatomy of an interconnection network router and science of interconnection network design will only grow in importance in the coming years. This book offers a detailed and comprehensive presentation of the basic principles of interconnection network design, clearly illustrating them with numerous examples, chapter exercises, and case studies. It incorporates hardware-level descriptions of concepts, allowing a designer to see all the steps of the process from abstract design to concrete implementation. Case studies throughout the book draw on extensive author experience in designing interconnection networks over a period of more than twenty years, providing real world examples of what works, and what doesn't. Tightly couples concepts with implementation costs to facilitate a deeper understanding of the tradeoffs in the design of a practical network. A set of examples and exercises in every chapter help the reader to fully understand all the implications of every design decision.

# Finite State Machines in Hardware Theory and Design (with VHDL and SystemVerilog)

*MIT Press A comprehensive guide to the theory and design of hardware-implemented finite state machines, with design examples developed in both VHDL and SystemVerilog languages. Modern, complex digital systems invariably include hardware-implemented finite state machines. The correct design of such parts is crucial for attaining proper system performance. This book offers detailed, comprehensive coverage of the theory and design for any category of hardware-implemented finite state machines. It describes crucial design problems that lead to incorrect or far from optimal implementation and provides examples of finite state machines developed in both VHDL and SystemVerilog (the successor of Verilog) hardware description languages. Important features include: extensive review of design practices for sequential digital circuits; a new division of all state machines into three hardware-based categories, encompassing all possible situations, with numerous practical examples provided in all three categories; the presentation of complete designs, with detailed VHDL and SystemVerilog codes, comments, and simulation results, all tested in FPGA devices; and exercise examples, all of which can be synthesized, simulated, and physically implemented in FPGA boards. Additional material is available on the book's Website. Designing a state machine in hardware is more complex than designing it in software. Although interest in hardware for finite state machines has grown dramatically in recent years, there is no comprehensive treatment of the subject. This book offers the most detailed coverage of finite state machines available. It will be essential for industrial designers of digital systems and for students of electrical engineering and computer science.*

## STRUCTURED COMPUTER ORGANIZATION Concrete Abstractions An Introduction to Computer

# Science Using Scheme

*Max Hailperin CONCRETE ABSTRACTIONS offers students a hands-on, abstraction-based experience of thinking like a computer scientist. This text covers the basics of programming and data structures, and gives first-time computer science students the opportunity to not only write programs, but to prove theorems and analyze algorithms as well. Students learn a variety of programming styles, including functional programming, assembly-language programming, and object-oriented programming (OOP). While most of the book uses the Scheme programming language, Java is introduced at the end as a second example of an OOP system and to demonstrate concepts of concurrent programming.*

# The Architecture of Computer Hardware, Systems Software, and Networking

# An Information Technology Approach

*John Wiley & Sons The Architecture of Computer Hardware, Systems Software and Networking is designed help students majoring in information technology (IT) and information systems (IS) understand the structure and operation of computers and computer-based devices. Requiring only basic computer skills, this accessible textbook introduces the basic principles of system architecture and explores current technological practices and trends using clear, easy-to-understand language. Throughout the text, numerous relatable examples, subject-specific illustrations, and in-depth case studies reinforce key learning points and show students how important concepts are applied in the real world. This fully-updated sixth edition features a wealth of new and revised content that reflects today's technological landscape. Organized into five parts, the book first explains the role of the computer in information systems and provides an overview of its components. Subsequent sections discuss the representation of data in the computer, hardware architecture and operational concepts, the basics of computer networking, system software and operating systems, and various interconnected systems and components. Students are introduced to the material using ideas already familiar to them, allowing them to gradually build upon what they have learned without being overwhelmed and develop a deeper knowledge of computer architecture.*

# Modern Processor Design Fundamentals of Superscalar Processors

*Waveland Press Conceptual and precise, Modern Processor Design brings together numerous microarchitectural techniques in a clear, understandable framework that is easily accessible to both graduate and undergraduate students. Complex practices are distilled into foundational principles to reveal the authors insights and hands-on experience in the effective design of contemporary high-performance micro-processors for mobile, desktop, and server markets. Key theoretical and foundational principles are presented in a systematic way to ensure comprehension of important implementation issues. The text presents fundamental concepts and foundational techniques such as processor design, pipelined processors, memory and I/O systems, and especially superscalar organization and implementations. Two case studies and an extensive survey of actual commercial superscalar processors reveal real-world developments in processor design and performance. A thorough overview of advanced instruction flow techniques, including developments in advanced branch predictors, is incorporated. Each chapter concludes with homework problems that will institute the groundwork for emerging techniques in the field and an introduction to multiprocessor systems.*

# Data Structures and Algorithms in Python

*Wiley Global Education Based on the authors' market leading data structures books in Java and C++, this textbook offers a comprehensive, definitive introduction to data structures in Python by authoritative authors. Data Structures and Algorithms in Python is the first authoritative object-oriented book available for the Python data structures course. Designed to provide a comprehensive introduction to data structures and algorithms, including their design, analysis, and implementation, the text will maintain the same general structure as Data Structures and Algorithms in Java and Data Structures and Algorithms in C++.*

# Information Retrieval Implementing and Evaluating

# Search Engines

*MIT Press* An introduction to information retrieval, the foundation for modern search engines, that emphasizes implementation and experimentation. Information retrieval is the foundation for modern search engines. This textbook offers an introduction to the core topics underlying modern search technologies, including algorithms, data structures, indexing, retrieval, and evaluation. The emphasis is on implementation and experimentation; each chapter includes exercises and suggestions for student projects. Wumpus—a multiuser open-source information retrieval system developed by one of the authors and available online—provides model implementations and a basis for student work. The modular structure of the book allows instructors to use it in a variety of graduate-level courses, including courses taught from a database systems perspective, traditional information retrieval courses with a focus on IR theory, and courses covering the basics of Web retrieval. In addition to its classroom use, Information Retrieval will be a valuable reference for professionals in computer science, computer engineering, and software engineering.

# Programming Many-Core Chips

*Springer Science & Business Media* This book presents new concepts, techniques and promising programming models for designing software for chips with "many" (hundreds to thousands) processor cores. Given the scale of parallelism inherent to these chips, software designers face new challenges in terms of operating systems, middleware and applications. This will serve as an invaluable, single-source reference to the state-of-the-art in programming many-core chips. Coverage includes many-core architectures, operating systems, middleware, and programming models.

# Numerical computing with IEEE floating point arithmetic

*SIAM* This title provides an easily accessible yet detailed discussion of IEEE Std 754-1985, arguably the most important standard in the computer industry. The result of an unprecedented cooperation between academic computer scientists and the cutting edge of industry, it is supported by virtually every modern computer. Other topics include the floating point architecture of the Intel microprocessors and a discussion of programming language support for the standard.

# The Elements of Computing Systems

# Building a Modern Computer from First Principles

*Mit Press This title gives students an integrated and rigorous picture of applied computer science, as it comes to play in the construction of a simple yet powerful computer system.*

## Computer Architecture

## Software Aspects, Coding, and Hardware

*CRC Press With the new developments in computer architecture, fairly recent publications can quickly become outdated. Computer Architecture: Software Aspects, Coding, and Hardware takes a modern approach. This comprehensive, practical text provides that critical understanding of a central processor by clearly detailing fundamentals, and cutting edge design features. With its balanced software/hardware perspective and its description of Pentium processors, the book allows readers to acquire practical PC software experience. The text presents a foundation-level set of ideas, design concepts, and applications that fully meet the requirements of computer organization and architecture courses. The book features a "bottom up" computer design approach, based upon the author's thirty years experience in both academe and industry. By combining computer engineering with electrical engineering, the author describes how logic circuits are designed in a CPU. The extensive coverage of a microprogrammed CPU and new processor design features gives the insight of current computer development. Computer Architecture: Software Aspects, Coding, and Hardware presents a comprehensive review of the subject, from beginner to advanced levels. Topics include:*

- o Two's complement numbers*
- o Integer overflow*
- o Exponent overflow and underflow*
- o Looping*
- o Addressing modes*
- o Indexing*
- o Subroutine linking*
- o I/O structures*
- o Memory mapped I/O*
- o Cycle stealing*
- o Interrupts*
- o Multitasking*
- o Microprogrammed CPU*
- o Multiplication tree*
- o Instruction queue*
- o Multimedia instructions*
- o Instruction cache*
- o Virtual memory*
- o Data cache*
- o Alpha chip*
- o Interprocessor communications*
- o Branch prediction*
- o Speculative loading*
- o Register stack*
- o JAVA virtual machine*
- o Stack machine principles*

## ICDSMLA 2019

# Proceedings of the 1st International Conference on Data Science, Machine Learning and Applications

*Springer Nature* This book gathers selected high-impact articles from the 1st International Conference on Data Science, Machine Learning & Applications 2019. It highlights the latest developments in the areas of Artificial Intelligence, Machine Learning, Soft Computing, Human-Computer Interaction and various data science & machine learning applications. It brings together scientists and researchers from different universities and industries around the world to showcase a broad range of perspectives, practices and technical expertise.

## Introduction to Computer Theory

*John Wiley & Sons Incorporated* Designed for undergraduate courses in computer theory, this textbook covers three areas: formal languages, automata theory and Turing machines. The author substitutes graphic representation for symbolic proofs, making it accessible even to students with little mathematical background.

## Computer Architecture

### A Quantitative Approach

*Morgan Kaufmann* *Computer Architecture: A Quantitative Approach, Sixth Edition* has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including

*new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry*

## ITJEMAST 10(7) 2019

*International Transaction Journal of Engineering, Management, & Applied Sciences & Technologies International Transaction Journal of Engineering, Management, & Applied Sciences & Technologies publishes a wide spectrum of research and technical articles as well as reviews, experiments, experiences, modelings, simulations, designs, and innovations from engineering, sciences, life sciences, and related disciplines as well as interdisciplinary/cross-disciplinary/multidisciplinary subjects. Original work is required. Article submitted must not be under consideration of other publishers for publications.*

## Performance Tuning of Scientific Applications

*CRC Press With contributions from some of the most notable experts in the field, Performance Tuning of Scientific Applications presents current research in performance analysis. The book focuses on the following areas. Performance monitoring: Describes the state of the art in hardware and software tools that are commonly used for monitoring and measuring performance and managing large quantities of data Performance analysis: Discusses modern approaches to computer performance benchmarking and presents results that offer valuable insight into these studies Performance modeling: Explains how researchers deduce accurate performance models from raw performance data or from other high-level characteristics of a scientific computation Automatic performance tuning: Explores ongoing research into automatic and semi-automatic techniques for optimizing computer programs to achieve superior performance on any computer platform Application tuning: Provides examples that show how the appropriate analysis of performance and some deft changes have resulted in extremely high performance Performance analysis has grown into a full-fledged, sophisticated field of empirical science. Describing useful research in modern performance science and engineering, this book helps real-world users of parallel computer systems to better understand both the performance vagaries arising in scientific applications and the practical means for improving performance. Read about the book on HPCwire and insideHPC*

# Inside the Machine

## An Illustrated Introduction to Microprocessors and Computer Architecture

*No Starch Press Om hvordan mikroprocessorer fungerer, med undersøgelse af de nyeste mikroprocessorer fra Intel, IBM og Motorola.*

## Computer Networks

*Pearson College Division Computer Networks is the ideal introduction to today's and tomorrow's networks. This classic best-seller has been totally rewritten to reflect the networks of the late 1990s and beyond. Author, educator, and researcher Andrew S. Tanenbaum, winner of the ACM Karl V. Karlstrom Outstanding Educator Award, carefully explains how networks work inside, from the hardware technology up through the most popular network applications. The book takes a structured approach to networking, starting at the bottom (the physical layer) and gradually working up to the top (the application layer). The topics covered include: \*Physical layer (e.g., copper, fiber, radio, and satellite communication) \*Data link layer (e.g., protocol principles, HDLC, SLIP, and PPP) \*MAC Sublayer (e.g., IEEE 802 LANs, bridges, new high-speed LANs) \*Network layer (e.g., routing, congestion control, internetworking, IPv6) \*Transport layer (e.g., transport protocol principles, TCP, network performance) \*Application layer (e.g., cryptography, email, news, the Web, Java, multimedia) In each chapter, the necessary principles are described in detail, followed by extensive examples taken from the Internet, ATM networks, and wireless*

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## Fundamentals of Computer Organization and Design

*Springer Science & Business Media A new advanced textbook/reference providing a comprehensive survey of hardware and software architectural principles and methods of computer systems organization and design. The book is suitable for a first course in computer organization. The style is similar to that of the author's book*

on assembly language in that it strongly supports self-study by students. This organization facilitates compressed presentation of material. Emphasis is also placed on related concepts to practical designs/chips. Topics: material presentation suitable for self-study; concepts related to practical designs and implementations; extensive examples and figures; details provided on several digital logic simulation packages; free MASM download instructions provided; and end-of-chapter exercises.

## Embedded Hardware: Know It All

*Newnes The Newnes Know It All Series takes the best of what our authors have written to create hard-working desk references that will be an engineer's first port of call for key information, design techniques and rules of thumb. Guaranteed not to gather dust on a shelf! Circuit design using microcontrollers is both a science and an art. This book covers it all. It details all of the essential theory and facts to help an engineer design a robust embedded system. Processors, memory, and the hot topic of interconnects (I/O) are completely covered. Our authors bring a wealth of experience and ideas; this is a must-own book for any embedded designer. \*A 360 degree view from best-selling authors including Jack Ganssle, Tammy Noergard, and Fred Eady \*Key facts, techniques, and applications fully detailed \*The ultimate hard-working desk reference: all the essential information, techniques, and tricks of the trade in one volume*

## The Financial Crisis Inquiry Report The Final Report of the National Commission on the Causes of the Financial and Economic Crisis in the United States Including Dissenting Views

*Cosimo, Inc. The Financial Crisis Inquiry Report, published by the U.S. Government and the Financial Crisis Inquiry Commission in early 2011, is the official government report on the United States financial collapse and the review of major financial institutions that bankrupted and failed, or would have without help from the government. The commission and the report were implemented after Congress passed an act in 2009 to review and prevent fraudulent activity. The report details, among other things, the periods before, during, and after the crisis, what led up to it, and analyses of subprime mortgage lending, credit expansion and banking policies, the collapse of companies like Fannie Mae and Freddie Mac, and the federal bailouts of Lehman and AIG. It also discusses the aftermath of the fallout and our current*

state. This report should be of interest to anyone concerned about the financial situation in the U.S. and around the world. THE FINANCIAL CRISIS INQUIRY COMMISSION is an independent, bi-partisan, government-appointed panel of 10 people that was created to "examine the causes, domestic and global, of the current financial and economic crisis in the United States." It was established as part of the Fraud Enforcement and Recovery Act of 2009. The commission consisted of private citizens with expertise in economics and finance, banking, housing, market regulation, and consumer protection. They examined and reported on "the collapse of major financial institutions that failed or would have failed if not for exceptional assistance from the government." News Dissector DANNY SCHECHTER is a journalist, blogger and filmmaker. He has been reporting on economic crises since the 1980's when he was with ABC News. His film *In Debt We Trust* warned of the economic meltdown in 2006. He has since written three books on the subject including *Plunder: Investigating Our Economic Calamity* (Cosimo Books, 2008), and *The Crime Of Our Time: Why Wall Street Is Not Too Big to Jail* (Disinfo Books, 2011), a companion to his latest film *Plunder The Crime Of Our Time*. He can be reached online at [www.newsdissector.com](http://www.newsdissector.com).

## Computer Networks

### A Systems Approach

*Elsevier Computer Networks: A Systems Approach, Fifth Edition*, explores the key principles of computer networking, with examples drawn from the real world of network and protocol design. Using the Internet as the primary example, this best-selling and classic textbook explains various protocols and networking technologies. The systems-oriented approach encourages students to think about how individual network components fit into a larger, complex system of interactions. This book has a completely updated content with expanded coverage of the topics of utmost importance to networking professionals and students, including P2P, wireless, network security, and network applications such as e-mail and the Web, IP telephony and video streaming, and peer-to-peer file sharing. There is now increased focus on application layer issues where innovative and exciting research and design is currently the center of attention. Other topics include network design and architecture; the ways users can connect to a network; the concepts of switching, routing, and internetworking; end-to-end protocols; congestion control and resource allocation; and end-to-end data. Each chapter includes a problem statement, which introduces issues to be examined; shaded sidebars that elaborate on a topic or introduce a related advanced topic; What's Next? discussions that deal with emerging issues in research, the commercial world, or society; and exercises. This book is written for graduate or upper-division undergraduate classes in computer networking. It will also be useful for industry professionals retraining for network-related assignments, as well as for network practitioners seeking to understand the workings of network protocols and the big picture of networking. Completely updated content with expanded coverage of the topics of utmost importance to networking

*professionals and students, including P2P, wireless, security, and applications  
Increased focus on application layer issues where innovative and exciting research  
and design is currently the center of attention Free downloadable network simulation  
software and lab experiments manual available*

# Introduction to High Performance Computing for Scientists and Engineers

*CRC Press Written by high performance computing (HPC) experts, Introduction to  
High Performance Computing for Scientists and Engineers provides a solid  
introduction to current mainstream computer architecture, dominant parallel  
programming models, and useful optimization strategies for scientific HPC. From  
working in a scientific computing center, the author*